## Claims:

1	1.	A method of compensating for output error in a sigma delta circuit,	
2	comprising:		
3		receiving an input signal;	
4		adding a first error voltage value, which is derived from an output	
5	signal, to the input signal;		
6		subtracting a second error value, which is derived from the adding of a	
7	first error voltage value, to the input signal from the input signal; and		
8 .		outputting an output signal result from the sigma delta circuit.	
1	2.	A sigma delta digital circuit configured to compensate for output error,	
2	comprising:		
3		an input for receiving an input signal;	
4		an output configured to output a output signal;	
5		a summation component configured to add a first error voltage value,	
6	which is derived from an output signal, to an incoming input signal; and		
7		a subtraction component configured to subtract a second error voltage	
8	value, where the	he second error voltage value is derived from the adding of a first error	
9	voltage value to an incoming input signal.		
1	3.	A sigma delta digital circuit according to Claim 2, further comprising a	
2	filter configured to filter an input signal according to a filter function, wherein the		
3	filter generates noise that distorts the filtered input signal, wherein the distortion		
4	results in the first error value.		
1	4.	A sigma delta digital circuit according to Claim 2, further comprising a	
2	filter configured to filter an input signal according to a filter function, wherein the		
3	filter generates noise that distorts the filtered input signal, wherein the distortion		
4	results in the second error value.		